

hifrog - Bug #5548

in Lattice: supposed to be SAT, but the lattice gives

29/09/2017 14:25 - Sepideh Asadi

Status:	New	Start date:	29/09/2017
Priority:	Low	Due date:	
Assignee:		% Done:	100%
Category:		Estimated time:	0.00 hour
Target version:		Spent time:	0.00 hour
Description			
/hifrog --load-sum-model modlattice_model_lra --logic qflra --load-summaries __summaries_simple_mod_lra --no-itp --unwind 10 --claim 1 ../benchmarks/modulus_false-no-overflow.c			
it includes bit-vector operators like a shift register. We should think of in the future to solve such issues with theoref			
Subtasks:			
Bug # 5549: .			Closed

History

#1 - 29/09/2017 16:40 - Sepideh Asadi

Sepideh Asadi wrote:

```
/hifrog --load-sum-model modlattice_model_lra --logic qflra --load-summaries __summaries_simple_mod_lra --no-itp --unwind 10 --claim 1  
../benchmarks/modulus_false-no-overflow.c
```

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```
/hifrog --load-sum-model modlattice_model_lra --logic qflra --load-summaries __summaries_simple_mod_lra --no-itp --unwind 2 --claim 1  
../benchmarks/sanfoundry_24_false-valid-deref.c
```